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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Toshimitsu Taniguchi et al. Art Unit : 2811
Serial No. : 09/652,044 Examiner : Samuel A. Gebremariam
Filed : August 31, 2000 Confirmation No.: 2951
Notice of Allowance Date: December 23, 2003
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING
THEREOF

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed December 23, 2003, enclosed are a completed issue fee transmittal form PTOL-85b and a check for \$1660 for the required issue fee and publication fee, including patent copies. Applicant also encloses Comments on Reasons for Allowance.

Applicant respectfully requests the Examiner to grant the Petition to Correct Inventorship, which was originally filed on May 2, 2002. Pursuant to a telephone conversation on March 19, 2004 with the undersigned attorney, the Examiner indicated he would act on the Petition as soon as the file was returned to him.

Applicant respectfully requests that the Examiner to send a copy of the decision on the Petition by facsimile to the undersigned attorney and to contact the undersigned attorney by telephone if there are any questions regarding the Petition.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 23, 2004

Date of Deposit

Paula T. Romeo

Signature

Paula T. Romeo

Typed or Printed Name of Person Signing Certificate

Applicant : Toshimitsu Taniguchi et al.
Serial No. : 09/652,044
Filed : August 31, 2000
Page : 2 of 2

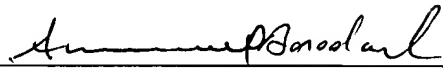


Attorney's Docket No.: 10417-039001 / F51-
125462M/SW

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/23/04



Samuel Borodach
Reg. No. 38,388

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Toshimitsu Taniguchi et al. Art Unit : 2811
Serial No. : 09/652,044 Examiner : Samuel A. Gebremariam
Filed : August 31, 2000 Confirmation No.: 8654
Notice of Allowance Date: December 31, 2002
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING
THEREOF

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON REASONS FOR ALLOWANCE

The Examiner's allowance of claims 6-8, 10-24, 27 and 28 is appreciated. The Applicant submits the following comments concerning the Examiner's Reasons for Allowance at page 2 of the Notice of Allowability. It is the subject matter of each claim as a whole that is patentable, not any particular feature recited in the claim(s). The Applicant does not concede that the reasons for allowance given by the Examiner are the only reasons that make the claims allowable and does not make any admission or concession concerning the Examiner's statement on pages 2 of the Notice of Allowability.

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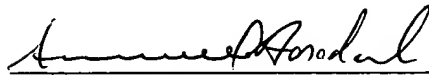
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